

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. 10/056,179
Filing Date January 22, 2002
Inventor Theodore M. Taylor
Assignee Micron Technology, Inc.
Group Art Unit 2814
Examiner Howard Weiss
Attorney Docket No. MI22-1824
Title Floating Gate Transistor With STI

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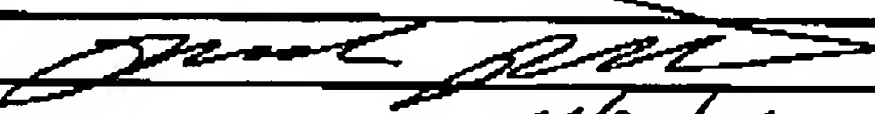
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/058,179
	Filing Date	January 22, 2002
	First Named Inventor	Theodore M. Taylor
	Art Unit	2814
	Examiner Name	Howard Weiss
Total Number of Pages in This Submission	Attorney Docket Number	MI22-1824

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm or Individual name	Mark S. Matkin, Reg. No. 32,268 Wells St. John P.S.	
Signature		
Date	4/23/04	

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Application Serial No. 10/056,179
Confirmation No. 9467
Filing Date January 22, 2002
Inventor Theodore M. Taylor
Assignee Micron Technology, Inc.
Group Art Unit 2814
Examiner Howard Weiss
Attorney's Docket No. MI22-1824
Customer No. 021567
Title Floating Gate Transistor With STI (as Amended)

RULE 312 RESPONSE

To: Mail Stop Issue Fee
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VIA FACSIMILE

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

Responsive to the Notice of Allowance dated March 25, 2004, Applicant
amends and remarks as follows:

AMENDMENTS

MI22\1824\M08.doc

1

Amendments to the Title

Please amend the title as follows:

Floating Gate Transistor With STI

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2